ABSTRACT OF THE DISCLOSURE

A digital-to-analog converting circuit includes first and second potential terminals, an output node, first and second resisters, first and second switches and control circuit. The first resisters are connected in series between a first node and the output node through first connecting points. Each of the first switches is connected between the first potential terminal and one of the first connecting points and the first node. The second resisters are connected in series between a second node and the output node through second connecting points. Each of the second switches is connected between the second potential terminal and one of the second connecting points and the second node. The control circuit controls the first and second switches.